



AiP74LVC2G132

Dual 2-Input Nand Gate With Schmitt-Trigger Inputs

Product Specification

Specification Revision History:

Version	Date	Description
2024-11-A0	2024-11	New
2025-05-A1	2025-05	Modify the parameters



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1、General Description

The AiP74LVC1G132 provides a dual 2-input NAND gate with schmitt-trigger inputs.

The input can be driven from either 3.3V or 5V devices. This feature allows the use of this device in a mixed 3.3V and 5V environment.

Features:

- Wide supply voltage range from 1.65V to 5.5V
- Inputs accept voltages to 5.5V
- $\pm 24\text{mA}$ output drive at 3.0V
- High-impedance when $V_{CC}=0\text{V}$
- Temperature range: -40°C to $+125^{\circ}\text{C}$
- Packaging information: TSSOP8/VSSOP8

Ordering Information:

Reel packing specifications:

Part number	Packaging form	Marking code	Reel quantity	Boxed reel quantity	Notes
AiP74LVC2G132TA8.TR	TSSOP8	AiPHD 2G132	3000 PCS/reel	3000 PCS/box	Dimensions of plastic enclosure: 3.0mm×3.0mm Pin spacing: 0.65mm
AiP74LVC2G132YA8.TR	VSSOP8	AiP HDXX	3000 PCS/reel	3000 PCS/box	Dimensions of plastic enclosure: 2.0mm×2.3mm Pin spacing: 0.50mm

Note 1: "XX" refers to variable content, meaning year and package batch serial number.

Note 2: If the physical information is inconsistent with the ordering information, please refer to the actual product.



2、Block Diagram And Pin Description

2.1、Block Diagram

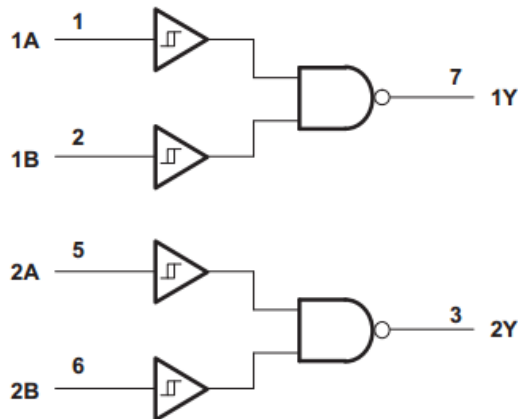
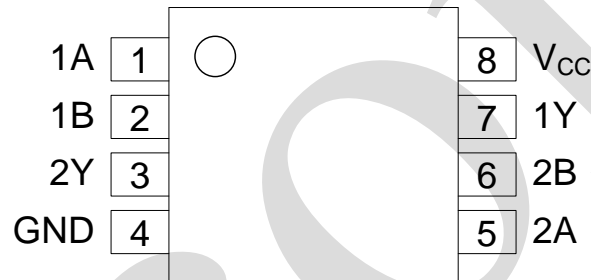


Figure 1. Logic symbol

2.2、Pin Configurations



2.3、Pin Description

Pin No.	Pin Name	Description
1	1A	data input
2	1B	data input
3	2Y	data output
4	GND	ground (0V)
5	2A	data input
6	2B	data input
7	1Y	data output
8	V _{CC}	supply voltage

2.4、Function Table

Input		Output
A	B	Y
L	L	H
L	H	H
H	L	H
H	H	L

Note: H=HIGH voltage level; L=LOW voltage level.



3、Electrical Parameter

3.1、Absolute Maximum Ratings

(Voltages are referenced to GND (ground=0V), unless otherwise specified.)

Parameter	Symbol	Conditions	Min.	Max.	Unit
supply voltage	V_{CC}	-	-0.5	+6.5	V
input voltage	V_I	-	-0.5	+6.5	V
output voltage	V_O	Active mode	-0.5	$V_{CC}+0.5$	V
		Power-down mode; $V_{CC}=0V$	-0.5	+6.5	V
supply current	I_{CC}	-	-	100	mA
ground current	I_{GND}	-	-100	-	mA
input clamping current	I_{IK}	$V_I < 0V$	-50	-	mA
output current	I_O	$V_O=0V$ to V_{CC}	-	± 50	mA
output clamping current	I_{OK}	$V_O > V_{CC}$ or $V_O < 0V$	-	± 50	mA
storage temperature	T_{stg}	-	-65	+150	°C
soldering temperature	T_L	10s	260		°C

3.2、Recommended Operating Conditions

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
supply voltage	V_{CC}	-	1.65	-	5.5	V
input voltage	V_I	-	0	-	5.5	V
output voltage	V_O	Active mode	0	-	V_{CC}	V
		Power-down mode; $V_{CC}=0V$	0	-	5.5	V
ambient temperature	T_{amb}	-	-40	-	+125	°C

3.3、Electrical Characteristics

3.3.1、DC Characteristics 1

($T_{amb}=-40^{\circ}C$ to $+85^{\circ}C$, voltages are referenced to GND (ground=0V), unless otherwise specified.)

Parameter	Symbol	V_{CC}	Conditions	Min.	Typ.	Max.	Unit
positive-going threshold voltage	V_{T+}	1.8V	-	0.79	-	1.16	V
		2.3V	-	1.11	-	1.56	V
		3.0V	-	1.5	-	1.87	V
		4.5V	-	2.16	-	2.74	V
		5.0V	-	2.61	-	3.33	V
negative-going threshold voltage	V_{T-}	1.8V	-	0.39	-	0.62	V
		2.3V	-	0.58	-	0.87	V
		3.0V	-	0.84	-	1.14	V
		4.5V	-	1.41	-	1.79	V
		5.0V	-	1.87	-	2.29	V
hysteresis voltage	V_H	1.8V	-	0.37	-	0.62	V
		2.3V	-	0.48	-	0.77	V
		3.0V	-	0.56	-	0.87	V
		4.5V	-	0.71	-	1.04	V
		5.0V	-	0.71	-	1.11	V
HIGH-level	V_{OH}	1.65V to 5.5V	$I_O=-100\mu A$	$V_{CC} - 0.1$	-	-	V



output voltage		1.65V	$I_O = -4\text{mA}$	1.2	1.54	-	V
		2.3V	$I_O = -8\text{mA}$	1.9	2.15	-	V
		2.7V	$I_O = -12\text{mA}$	2.2	2.50	-	V
		3.0V	$I_O = -24\text{mA}$	2.3	2.62	-	V
		4.5V	$I_O = -32\text{mA}$	3.8	4.11	-	V
LOW-level output voltage	V_{OL}	1.65V to 5.5V	$I_O = 100\mu\text{A}$	-	-	0.10	V
		1.65V	$I_O = 4\text{mA}$	-	0.07	0.45	V
		2.3V	$I_O = 8\text{mA}$	-	0.12	0.30	V
		2.7V	$I_O = 12\text{mA}$	-	0.17	0.40	V
		3.0V	$I_O = 24\text{mA}$	-	0.33	0.55	V
		4.5V	$I_O = 32\text{mA}$	-	0.39	0.55	V
input leakage current	I_I	0V to 5.5V	$V_I = 5.5\text{V}$ or GND	-	-	± 1	μA
OFF-state output current	I_{OZ}	3.6V	$V_I = V_{IH}$ or V_{IL} ; $V_O = 5.5\text{V}$ or GND	-	-	± 2	μA
power-off leakage current	I_{OFF}	0V	V_I or $V_O = 5.5\text{V}$	-	-	± 2	μA
supply current	I_{CC}	1.65V to 5.5V	$V_I = V_{CC}$ or GND; $I_O = 0\text{A}$	-	-	4	μA
additional supply current	ΔI_{CC}	2.7V to 5.5V	per input pin; $V_I = V_{CC} - 0.6\text{V}$; $I_O = 0\text{A}$	-	-	500	μA

Note: All typical values are measured at $T_{amb} = 25^\circ\text{C}$.

3.3.2、DC Characteristics 2

($T_{amb} = -40^\circ\text{C}$ to $+125^\circ\text{C}$, voltages are referenced to GND (ground = 0V), unless otherwise specified.)

Parameter	Symbol	V_{CC}	Conditions	Min.	Typ.	Max.	Unit
positive-going threshold voltage	V_{T+}	1.8V	-	0.79	-	1.16	V
		2.3V	-	1.11	-	1.56	V
		3.0V	-	1.5	-	1.87	V
		4.5V	-	2.16	-	2.74	V
		5.0V	-	2.61	-	3.33	V
negative-going threshold voltage	V_{T-}	1.8V	-	0.39	-	0.62	V
		2.3V	-	0.58	-	0.87	V
		3.0V	-	0.84	-	1.14	V
		4.5V	-	1.41	-	1.79	V
		5.0V	-	1.87	-	2.29	V
hysteresis voltage	V_H	1.8V	-	0.37	-	0.62	V
		2.3V	-	0.48	-	0.77	V
		3.0V	-	0.56	-	0.87	V
		4.5V	-	0.71	-	1.04	V
		5.0V	-	0.71	-	1.11	V
HIGH-level output voltage	V_{OH}	1.65V to	$I_O = -100\mu\text{A}$	$V_{CC} - 0.1$	-	-	V
		1.65V	$I_O = -4\text{mA}$	0.95	-	-	V
		2.3V	$I_O = -8\text{mA}$	1.7	-	-	V



		2.7V	$I_O = -12\text{mA}$	1.9	-	-	V
		3.0V	$I_O = -24\text{mA}$	2.0	-	-	V
		4.5V	$I_O = -32\text{mA}$	3.4	-	-	V
LOW-level output voltage	V_{OL}	1.65V to	$I_O = 100\mu\text{A}$	-	-	0.10	V
		1.65V	$I_O = 4\text{mA}$	-	-	0.70	V
		2.3V	$I_O = 8\text{mA}$	-	-	0.45	V
		2.7V	$I_O = 12\text{mA}$	-	-	0.60	V
		3.0V	$I_O = 24\text{mA}$	-	-	0.80	V
		4.5V	$I_O = 32\text{mA}$	-	-	0.80	V
input leakage current	I_I	0V to 5.5V	$V_I = 5.5\text{V}$ or GND	-	-	± 1	μA
OFF-state output current	I_{OZ}	3.6V	$V_I = V_{IH}$ or V_{IL} ; $V_O = 5.5\text{V}$ or GND	-	-	± 2	μA
power-off leakage current	I_{OFF}	0V	V_I or $V_O = 5.5\text{V}$	-	-	± 2	μA
supply current	I_{CC}	1.65V to 5.5V	$V_I = V_{CC}$ or GND; $I_O = 0\text{A}$	-	-	4	μA
additional supply current	ΔI_{CC}	2.7V to 5.5V	per input pin; $V_I = V_{CC} - 0.6\text{V}$; $I_O = 0\text{A}$	-	-	500	μA

3.3.3、AC Characteristics 1

($T_{amb} = -40^\circ\text{C}$ to $+85^\circ\text{C}$, $C_L = 30\text{pF}$ or 50pF , voltages are referenced to GND (ground = 0V), unless otherwise specified.)

Parameter	Symbol	V_{CC}	Conditions	Min.	Typ.	Max.	Unit
A, B to Y propagation delay	t_{pd}	1.8V \pm 0.15V	see Figure 3	-	-	32	ns
		2.5V \pm 0.2V		-	-	15	ns
		3.3V \pm 0.3V		-	-	12	ns
		5V \pm 0.5V		-	-	10	ns

3.3.4、AC Characteristics 2

($T_{amb} = -40^\circ\text{C}$ to $+125^\circ\text{C}$, $C_L = 30\text{pF}$ or 50pF , voltages are referenced to GND (ground = 0V), unless otherwise specified.)

Parameter	Symbol	V_{CC}	Conditions	Min.	Typ.	Max.	Unit
A, B to Y propagation delay	t_{pd}	1.8V \pm 0.15V	see Figure 3	-	-	34	ns
		2.5V \pm 0.2V		-	-	17	ns
		3.3V \pm 0.3V		-	-	18	ns
		5V \pm 0.5V		-	-	12	ns



4、Testing Circuit

4.1、AC Testing Circuit

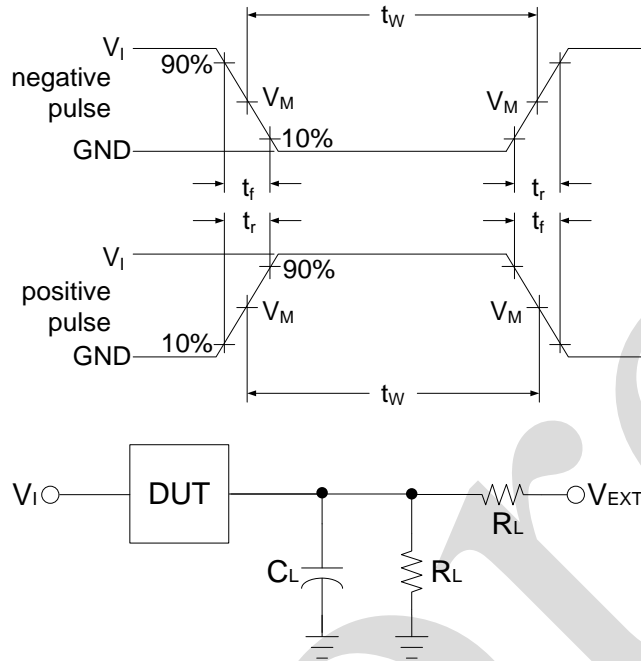


Figure 2. AC Testing Circuit

Definitions for test circuit:

R_L =Load resistance.

C_L =Load capacitance including jig and probe capacitance.

4.2、Test Data

Supply voltage	Input		Load		V_{EXT}
V_{CC}	V_I	t_r, t_f	C_L	R_L	t_{PLH}, t_{PHL}
$1.8V \pm 0.15V$	V_{CC}	$\leq 2ns$	30pF	1k Ω	open
$2.5V \pm 0.2V$	V_{CC}	$\leq 2ns$	30pF	500 Ω	open
$3.3V \pm 0.3V$	3V	$\leq 2.5ns$	50pF	500 Ω	open
$5V \pm 0.5V$	V_{CC}	$\leq 2.5ns$	50pF	500 Ω	open



4.3、 AC Testing Waveforms

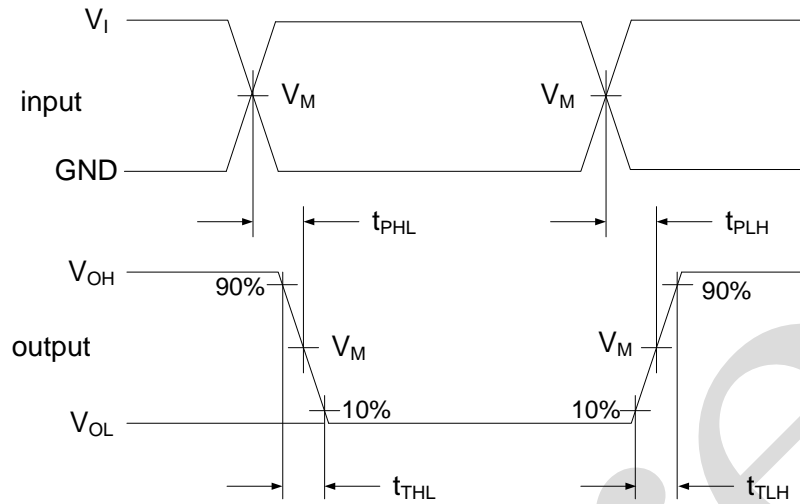


Figure 3. The data input (A) to output (Y) propagation delays

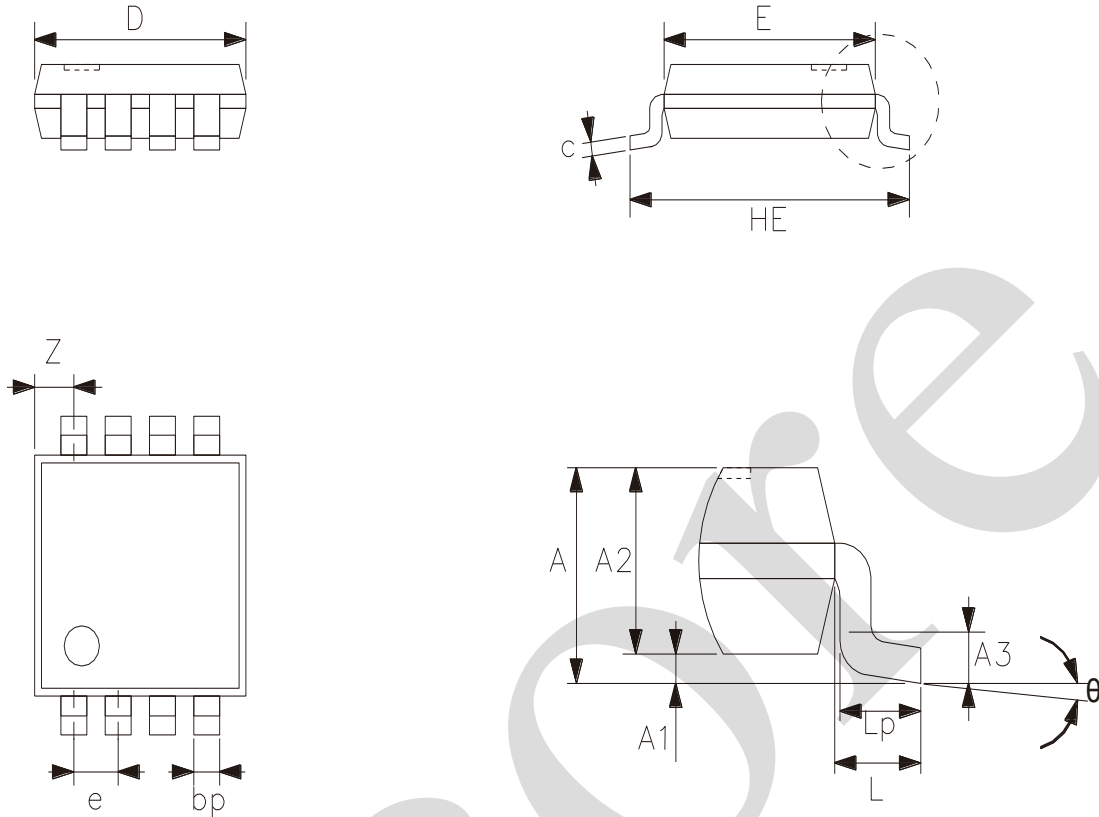
4.4、 Measurement Points

Supply voltage	Input	Output
V_{CC}	V_M	V_M
$1.8V \pm 0.15V$	$0.5 \times V_{CC}$	$0.5 \times V_{CC}$
$2.5V \pm 0.2V$	$0.5 \times V_{CC}$	$0.5 \times V_{CC}$
$3.3V \pm 0.3V$	1.5V	1.5V
$5V \pm 0.5V$	$0.5 \times V_{CC}$	$0.5 \times V_{CC}$



5、Package Information

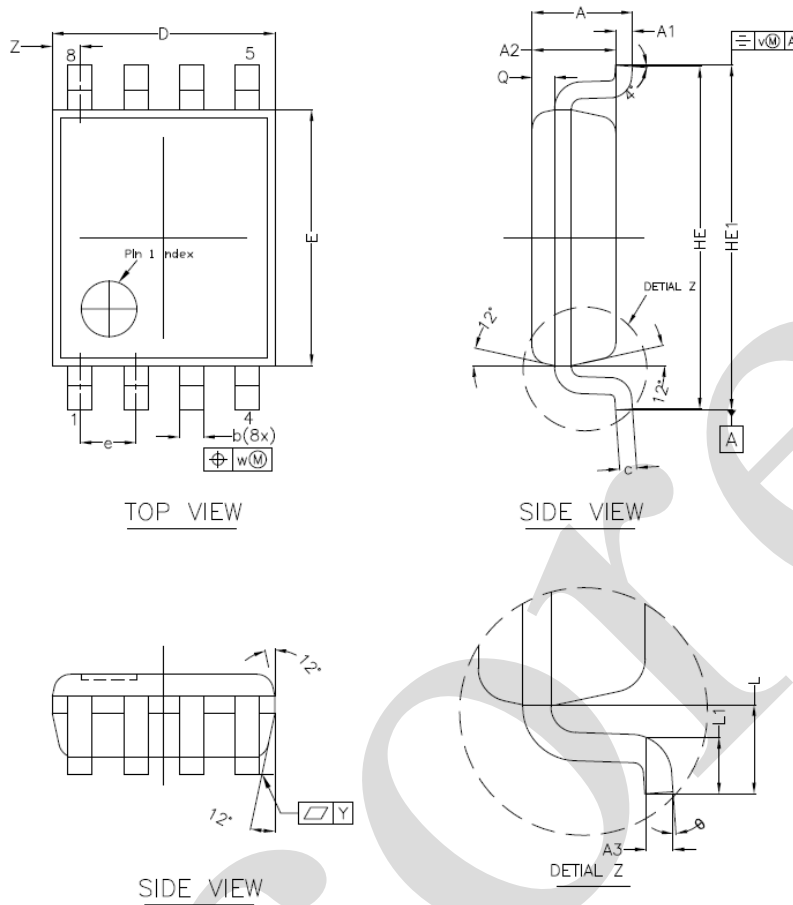
5.1、TSSOP8



2023/12/A	Dimensions In Millimeters	
Symbol	Min	Max
A	—	1.10
A1	0	0.15
A2	0.75	0.95
A3	0.25	
bp	0.22	0.38
c	0.08	0.18
D	2.90	3.10
E	2.90	3.10
HE	3.90	4.10
L	0.50	
Lp	0.33	0.47
e	0.65	
Z	0.35	0.70
θ	0°	8°



5.2、VSSOP8



NOTES
 1.0 COP
 DIE ATTA
 2.0 D E

2023/12/A	Dimensions In Millimeters	
Symbol	Min	Max
A	—	1.00
A1	0.00	0.15
A2	0.60	0.85
A3	0.12	
Q	0.19	0.21
b	0.17	0.27
c	0.08	0.23
D	1.90	2.10
E	2.20	2.40
HE	3.00	3.20
HE1	3.00	3.40
e	0.50	
L	0.40	
L1	0.15	0.40
Y	0.10	
Z	0.10	0.40
θ	0°	8°



6、 Statements And Notes

6.1、 The name and content of Hazardous substances or Elements in the product

Part name	Hazardous substances or Elements									
	Lead and lead compounds	Mercury and mercury compounds	Cadmium and cadmium compounds	Hexavalent chromium compounds	Polybrominated biphenyls	Polybrominated biphenyl ethers	Dibutyl phthalate	Butylbenzyl phthalate	Di-2-ethylhexyl phthalate	Diisobutyl phthalate
Lead frame	○	○	○	○	○	○	○	○	○	○
Plastic resin	○	○	○	○	○	○	○	○	○	○
Chip	○	○	○	○	○	○	○	○	○	○
The lead	○	○	○	○	○	○	○	○	○	○
Plastic sheet installed	○	○	○	○	○	○	○	○	○	○
explanation	○: Indicates that the content of hazardous substances or elements in the detection limit of the following the SJ/T11363-2006 standard. ×: Indicates that the content of hazardous substances or elements exceeding the SJ/T11363-2006 Standard limit requirements.									

6.2、 Notes

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