



AiP74CBTLV3257

4-Bit 1-of-2 FET

Multiplexer/Demultiplexer

Product specification

Manual release resume:

Version	Date	Description
2023-08-A1	2023-08	New
2024-05-A2	2024-05	Modify the content
2024-07-A3	2024-07	Modify the content
2026-05-A4	2026-05	Add VQFN16 package form
2026-07-A5	2026-07	Add MIS16 package form



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1、 General Description

AiP74CBTLV3257 is a 4-Bit 1-of-2 FET Multiplexer/Demultiplexer. The circuit has a common selection port (S) and an enable control port ($\overline{OE}$).

To ensure the high-impedance OFF-state during power up or power down, $\overline{OE}$ should be tied to V_{CC} through a pull-up resistor.

This device is fully specified for partial power-down applications using I_{OFF} . The I_{OFF} circuitry disables the output, preventing the damaging backflow current through the device when it is powered down.

Features:

- supply voltage range: 2.3V to 3.6V
- On-resistance: 2.8Ω (typical) at $V_{CC}=3.3V$.
- Rail-to-rail switching on I/O ports
- I_{OFF} circuitry provides partial Power-down mode operation
- Temperature range: -40°C to $+125^{\circ}\text{C}$
- Packaging information: SOP16/SSOP16/TSSOP16/DHVQFN16/VQFN16/MIS16

**Ordering information:****Tube packing specifications:**

Part number	Packaging form	Marking code	Tube quantity	Boxed tube quantity	Boxed quantity	Notes
AiP74CBTLV3257 SA16.TB	SOP16	74CBTLV3257	50 PCS/tube	200 Tube/box	10000 PCS/ box	Dimensions of plastic enclosure: 10.0mm×3.9mm Pin spacing: 1.27mm
AiP74CBTLV3257 VB16.TB	SSOP16	74CBTLV3257	100 PCS/tube	100 Tube/box	10000 PCS/ box	Dimensions of plastic enclosure: 4.9mm×3.9mm Pin spacing: 0.635mm
AiP74CBTLV3257 TA16.TB	TSSOP16	74CBTLV3257	96 PCS/tube	200 Tube/box	19200 PCS/ box	Dimensions of plastic enclosure: 5.0mm×4.4mm Pin spacing: 0.65mm

Reel packing specifications:

Part number	Packaging form	Marking code	Reel quantity	Boxed reel quantity	Notes
AiP74CBTLV3257 SA16.TR	SOP16	74CBTLV3257	4000 PCS/reel	8000 PCS/box	Dimensions of plastic enclosure: 10.0mm×3.9mm Pin spacing:1.27mm
AiP74CBTLV3257 VB16.TR	SSOP16	74CBTLV3257	4000 PCS/reel	8000 PCS/box	Dimensions of plastic enclosure: 4.9mm×3.9mm Pin spacing:0.635mm
AiP74CBTLV3257 TA16.TR	TSSOP16	74CBTLV3257	5000 PCS/reel	10000 PCS/box	Dimensions of plastic enclosure: 5.0mm×4.4mm Pin spacing:0.65mm
AiP74CBTLV3257 QE16.TR	DHVQFN16	TLV3257	3000 PCS/reel	3000 PCS/box	Dimensions of plastic enclosure: 3.5mm×2.5mm Pin spacing: 0.5mm
AiP74CBTLV3257 QK16.TR	VQFN16	CBTLV3257	1500 PCS/reel	15000 PCS/box	Dimensions of plastic enclosure: 4.0mm×3.5mm Pin spacing: 0.5mm
AiP74CBTLV3257 MIS16.TR	MIS16	CBTLV 3257	3000 PCS/reel	30000 PCS/box	Dimensions of plastic enclosure: 2.6mm×1.8mm Pin spacing:0.4mm

Note: If the material object is inconsistent with the ordering information, please take the material object as the standard.



2、Block Diagram and Pin Description

2.1、Block Diagram

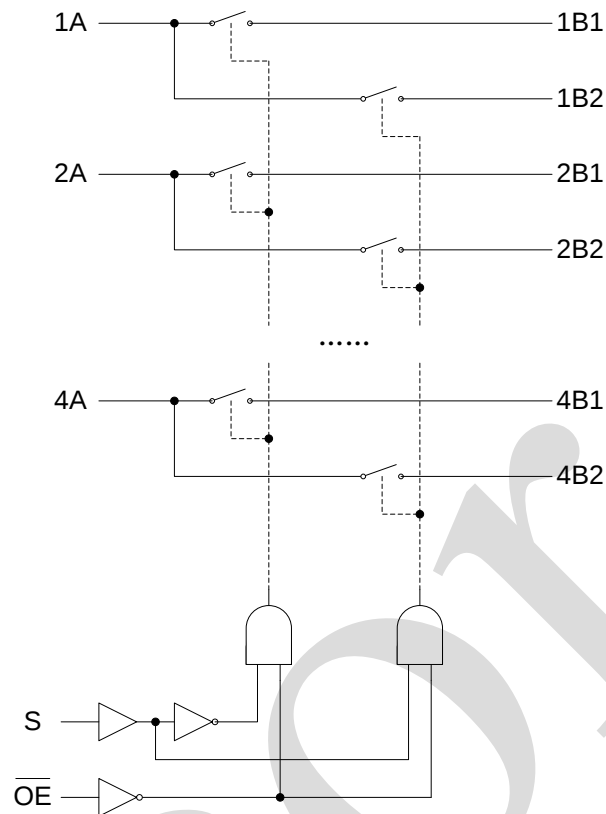
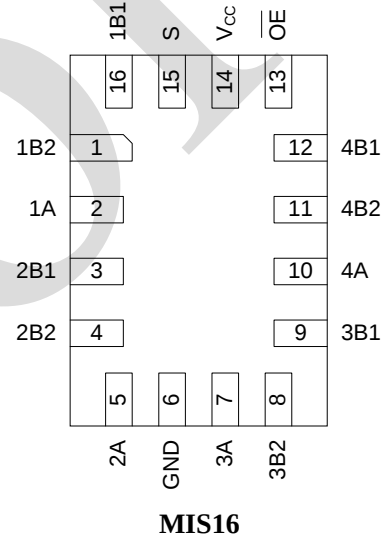
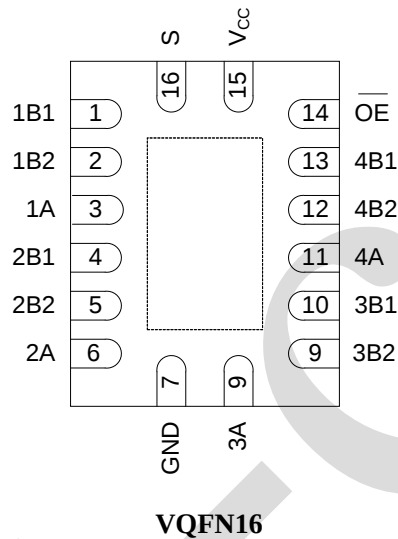
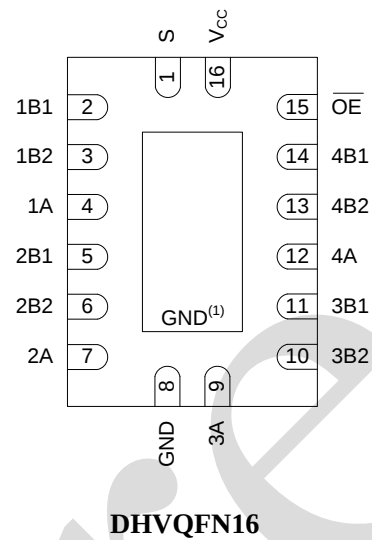
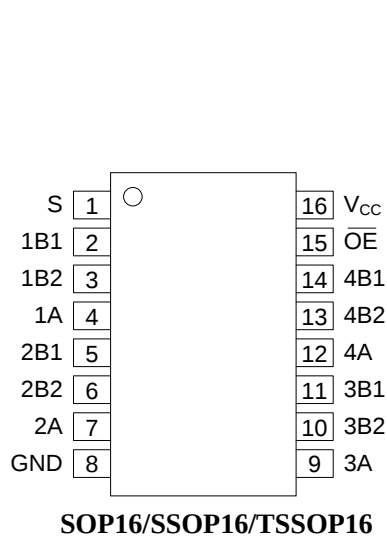


Figure 1. Logic diagram



2.2、Pin Configurations



Note:

(1) If it is soldered the solder land should remain floating or be connected to GND.



2.3、Pin Description

Pin No.			Pin Name	Description
SOP16/SSOP16/TSSOP16/ DHVQFN16	VQFN16	MIS16		
1	16	15	S	Select input
2	1	16	1B1	1B1 input/output
3	2	1	1B2	1B2 input/output
4	3	2	1A	1A input/output
5	4	3	2B1	2B1 input/output
6	5	4	2B2	2B2 input/output
7	6	5	2A	2A input/output
8	7	6	GND	Ground (0V)
9	8	7	3A	3A input/output
10	9	8	3B2	3B2 input/output
11	10	9	3B1	3B1 input/output
12	11	10	4A	4A input/output
13	12	11	4B2	4B2 input/output
14	13	12	4B1	4B1 input/output
15	14	13	OE	Output Enable Input (Active Low)
16	15	14	V _{CC}	supply voltage

2.4、Function Table

input		Function switch
OE	S	
L	L	nA=nB1
L	H	nA=nB2
H	X	nA and nB are not connected.

Note: H=HIGH voltage level; L=LOW voltage level; X=don't care.

3、Electrical Parameter

3.1、Absolute Maximum Ratings

Parameter	Symbol	Conditions	Min	Max	Unit
Supply voltage	V _{CC}	-	-0.5	+4.6	V
Input voltage	V _I	Control inputs	-0.5	+4.6	V
Switchvoltage	V _{SW}	Enable and disable mode	-0.5	V _{CC} +0.5	V
Input clamping current	I _{IK}	V _I <-0.5V	-50	-	mA
Switch clamping current	I _{SK}	V _I <-0.5V	-50	-	mA
Switchcurrent	I _{SW}	V _{SW} =0 to V _{CC}	-	±128	mA
Supply current	I _{CC}	-	-	±100	mA
Ground current	I _{GND}	-	-100	-	mA
Storage temperature	T _{stg}	-	-65	150	°C
Soldering temperature	T _L	10s	260		°C

**3.2、Recommended Operating Conditions**

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
Supply voltage	V_{CC}	-	2.3	-	3.6	V
Input voltage	V_I	-	0	-	3.6	V
Switch voltage	V_{SW}	Enable and disable mode	0	-	V_{CC}	V
Ambient temperature	T_{amb}	-	-40	-	+125	°C

3.3、Electrical Characteristics**3.3.1、DC Characteristics 1**(T_{amb}=-40°C to +85°C, GND=0V, unless otherwise specified)

Parameter	Symbol	supply voltage	Conditions	Min.	Typ.	Max.	Unit
High-level input voltage	V_{IH}	2.3V to 2.7V	-	1.7	-	-	V
		3.0V to 3.6V	-	2.0	-	-	V
Low-level input voltage	V_{IL}	2.3V to 2.7V	-	-	-	0.7	V
		3.0V to 3.6V	-	-	-	0.9	V
Input leakage current	I_I	3.6V	OE, S pin; $V_I = GND$ to V_{CC}	-	-	±1	uA
Off-state leakage current	$I_S(OFF)$	3.6V	-	-	-	±1	uA
ON-state leakage current	$I_S(ON)$	3.6V	-	-	-	±1	uA
Power-off leakage current	I_{OFF}	0V	V_I or $V_O = 0V \sim 3.6V$	-	-	±10	uA
Supply current	I_{CC}	3.6V	$V_I = V_{CC}$ or GND; $I_O = 0A$; $V_{SW} = GND$ or V_{CC}	-	-	10	uA
Additional supply current	ΔI_{CC}	3.6V	OE, S pin; $V_I = V_{CC} - 0.6V$; $V_{SW} = GND$ or V_{CC}	-	-	300	uA
On resistance	R_{ON}	2.3V to 2.7V See Figure 2-4	$I_{SW} = 64mA$; $V_I = 0V$	-	3.3	8	Ω
			$I_{SW} = 24mA$; $V_I = 0V$	-	3.2	8	Ω
			$I_{SW} = 15mA$; $V_I = 1.7V$	-	10.8	40	Ω
		3.0V to 3.6V See Figure 5-7	$I_{SW} = 64mA$; $V_I = 0V$	-	2.8	7	Ω
			$I_{SW} = 24mA$; $V_I = 0V$	-	2.8	7	Ω
			$I_{SW} = 15mA$; $V_I = 2.4V$	-	7.1	15	Ω

Note:

[1] All typical values are measured at $V_{CC} = 2.5V, 3.3V$ (unless otherwise specified) and $T_{amb} = 25^\circ C$.

**3.3.2、DC Characteristics 2**(T_{amb}= -40℃ to +125℃, GND=0V, unless otherwise specified)

Parameter	Symbol	supply voltage	Conditions	Min.	Typ.	Max.	Unit
High-level input voltage	V _{IH}	2.3V to 2.7V	-	1.7	-	-	V
		3.0V to 3.6V	-	2.0	-	-	V
Low-level input voltage	V _{IL}	2.3V to 2.7V	-	-	-	0.7	V
		3.0V to 3.6V	-	-	-	0.9	V
Input leakage current	I _I	3.6V	OE, S pin; V _I =GND to V _{CC}	-	-	±20	uA
Off-state leakage current	I _S (OFF)	3.6V	-	-	-	±20	uA
ON-state leakage current	I _S (ON)	3.6V	-	-	-	±20	uA
Power-off leakage current	I _{OFF}	0V	V _I or V _O =0V to 3.6V	-	-	±50	uA
Supply current	I _{CC}	3.6V	V _I =V _{CC} or GND; I _O =0A; V _{sw} =GND or V _{CC}	-	-	50	uA
Additional supply current	ΔI _{CC}	3.6V	OE, S pin; V _I =V _{CC} -0.6V; V _{sw} =GND or V _{CC}	-	-	2000	uA
On resistance	R _{ON}	2.3V to 2.7V See Figure 2-4	I _{sw} =64mA; V _I =0V	-	-	15	Ω
			I _{sw} =24mA; V _I =0V	-	-	15	Ω
			I _{sw} =15mA; V _I =1.7V	-	-	60	Ω
		3.0V to 3.6V See Figure 5-7	I _{sw} =64mA; V _I =0V	-	-	11	Ω
			I _{sw} =24mA; V _I =0V	-	-	11	Ω
			I _{sw} =15mA; V _I =2.4V	-	-	25.5	Ω

3.3.3、AC Characteristics 1(T_{amb}= -40℃ to +85℃, GND=0V, unless otherwise specified)

Parameter	Symbol	supply voltage	Conditions	Min.	Typ.	Max.	Unit
Propagation delay	t _{PLH} , t _{PHL}	2.3V to 2.7V	nA to nBn nBn to nA See Figure 9	-	-	0.15	ns
		3.0V to 3.6V		-	-	0.15	ns
		2.3V to 2.7V	S to nA See Figure 9	-	3.8	6.1	ns
		3.0V to 3.6V		-	3.2	5.3	ns
Enable time	t _{PZH} , t _{PZL}	2.3V to 2.7V	OE to nA/nBn See Figure 10	-	2.2	5.6	ns
		3.0V to 3.6V		-	2.0	5.0	ns
		2.3V to 2.7V	Sn to nBn See Figure 10	-	3.5	6.1	ns
		3.0V to 3.6V		-	3.0	5.3	ns
Disable time	t _{PLZ} , t _{PHZ}	2.3V to 2.7V	OE to nA/nBn See Figure 10	-	2.6	5.5	ns
		3.0V to 3.6V		-	3.1	5.5	ns
		2.3V to 2.7V	S to nBn See Figure 10	-	2.6	4.8	ns
		3.0V to 3.6V		-	3.2	4.5	ns

Note:

[1] All typical values are measured at V_{CC}=2.5V/3.3V (unless otherwise specified) and T_{amb}=25℃.



3.3.4、AC Characteristics 2

($T_{amb} = -40^{\circ}\text{C}$ to $+125^{\circ}\text{C}$, GND=0V, unless otherwise specified)

Parameter	Symbol	supply voltage	Conditions	Min.	Typ.	Max.	Unit
Propagation delay	t_{PLH} , t_{PHL}	2.3V to 2.7V	nA to nBn	-	-	0.25	ns
		3.0V to 3.6V	nBn to nA	-	-	0.25	ns
		2.3V to 2.7V	S to nA	-	-	6.7	ns
		3.0V to 3.6V	See Figure 9	-	-	5.8	ns
Enable time	t_{PZH} , t_{PZL}	2.3V to 2.7V	$\overline{\text{OE}}$ to nA/nBn	-	-	6.2	ns
		3.0V to 3.6V	See Figure 10	-	-	5.5	ns
		2.3V to 2.7V	Sn to nBn	-	-	6.7	ns
		3.0V to 3.6V	See Figure 10	-	-	5.8	ns
Disable time	t_{PLZ} , t_{PHZ}	2.3V to 2.7V	$\overline{\text{OE}}$ to nA/nBn	-	-	6.1	ns
		3.0V to 3.6V	See Figure 10	-	-	6.1	ns
		2.3V to 2.7V	S to nBn	-	-	5.3	ns
		3.0V to 3.6V	See Figure 10	-	-	5.0	ns

4、Characteristic curve

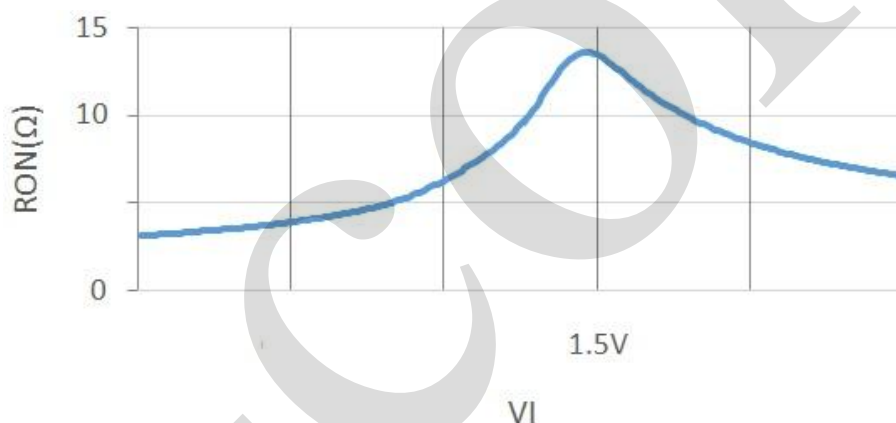


Figure 2. ON resistance as a function of input voltage ($V_{CC}=2.5\text{V}$, $I_{SW}=15\text{mA}$, $T_{amb}=25^{\circ}\text{C}$)

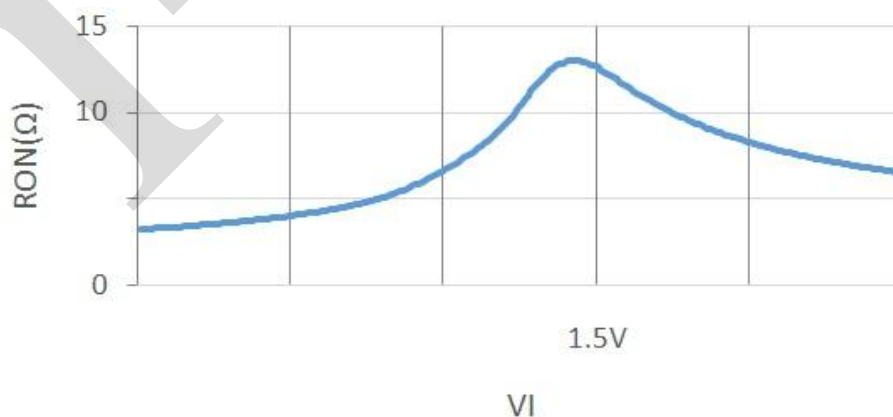


Figure 3. ON resistance as a function of input voltage ($V_{CC}=2.5\text{V}$, $I_{SW}=24\text{mA}$, $T_{amb}=25^{\circ}\text{C}$)

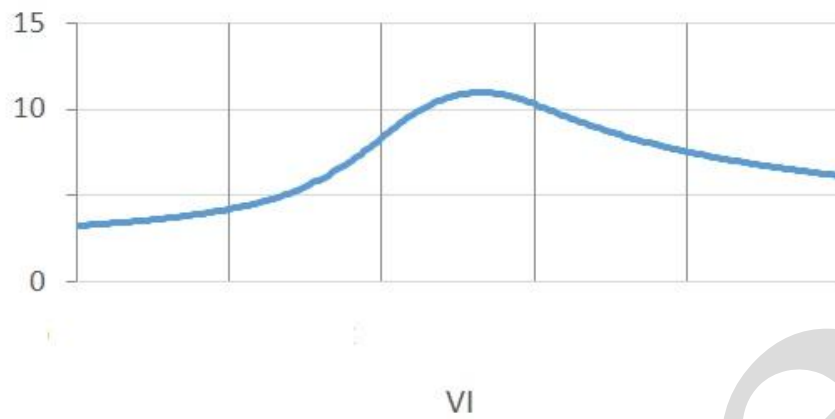


Figure 4. ON resistance as a function of input voltage ($V_{CC}=2.5V$, $I_{SW}=64mA$, $T_{amb}=25^{\circ}C$)

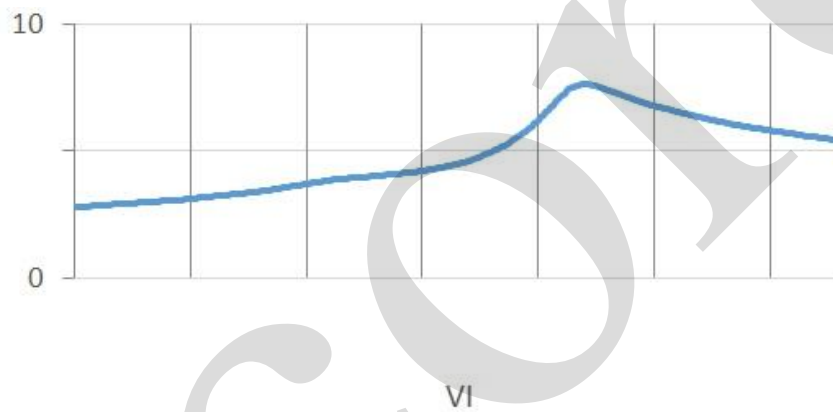


Figure 5. ON resistance as a function of input voltage ($V_{CC}=3.3V$, $I_{SW}=15mA$, $T_{amb}=25^{\circ}C$)

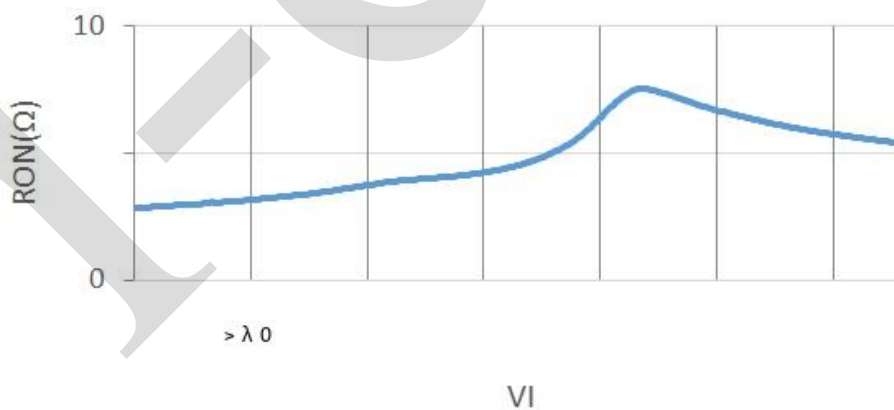


Figure 6. ON resistance as a function of input voltage ($V_{CC}=3.3V$, $I_{SW}=24mA$, $T_{amb}=25^{\circ}C$)

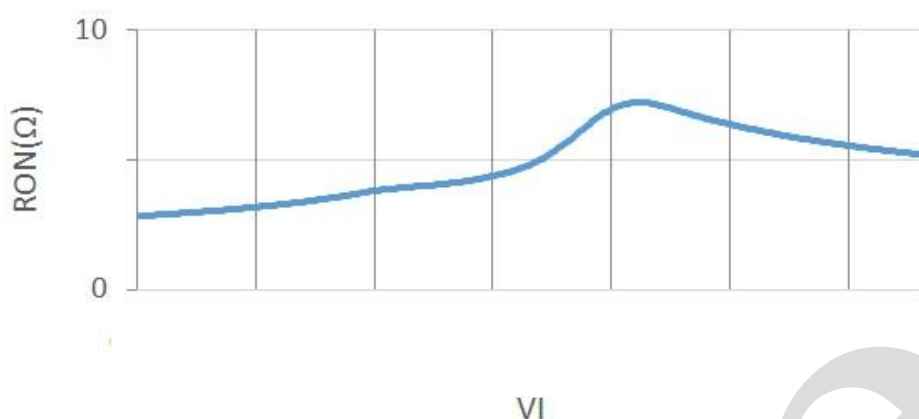


Figure 7. ON resistance as a function of input voltage ($V_{CC}=3.3V$, $I_{SW}=64mA$, $T_{amb}=25^{\circ}C$)

5、Testing circuit

5.1、AC testing circuit

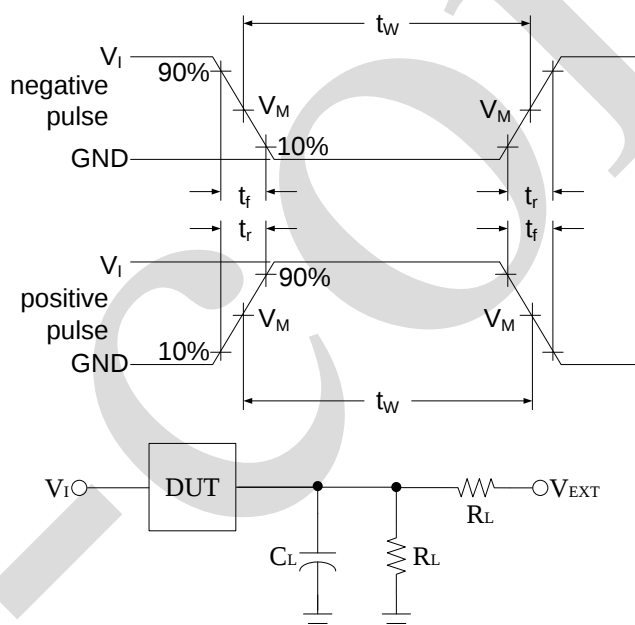


Figure 8. Test circuit for measuring switching times

Definitions for test circuit:

R_L =Load resistance.

C_L =Load capacitance including jig and probe capacitance.

5.2. Test data

supply voltage	input		load		V_{EXT}		
V_{CC}	V_I	t_r, t_f	C_L	R_L	t_{PLH}, t_{PHL}	t_{PHZ}, t_{PZH}	t_{PLZ}, t_{PZL}
2.3V~2.7V	V_{CC}	$\leq 3.0ns$	30pF	500Ω	open	GND	$2 \times V_{CC}$
3.0V~3.6V	V_{CC}	$\leq 3.0ns$	50pF	500Ω	open	GND	$2 \times V_{CC}$

The diagram illustrates the timing characteristics of a CMOS inverter. The input signal V_I is a square wave switching between a high level and ground (GND). The output signal V_O is a square wave switching between a high level V_{OH} and a low level V_{OL} . The switching threshold voltage V_M is indicated on both waveforms. The propagation delay from input high to output low is t_{PHL} , and from input low to output high is t_{PLH} . The transition times at the output, measured between 90% and 10% of the output swing, are t_{THL} (for falling) and t_{TLH} (for rising).

The diagram illustrates the timing characteristics of a 3-state buffer. It shows three signals: enable input, output LOW-to-OFF and OFF-to-LOW, and output HIGH-to-OFF and OFF-to-HIGH. The enable input is a square wave that transitions between high and low states. The output signals are shown during the enable transitions. The timing parameters are defined as follows:

- t_{PLZ} : Propagation delay from enable input rising edge to output LOW-to-OFF.
- t_{PZL} : Propagation delay from enable input falling edge to output LOW-to-OFF.
- t_{PHZ} : Propagation delay from enable input rising edge to output HIGH-to-OFF.
- t_{PZH} : Propagation delay from enable input falling edge to output HIGH-to-OFF.

The output signals are labeled with V_M (maximum output voltage), V_{OL} (output low voltage), V_{OH} (output high voltage), and V_X (output voltage at t_{PLZ}). The output signals are also labeled with V_M (maximum output voltage), V_{OL} (output low voltage), V_{OH} (output high voltage), and V_Y (output voltage at t_{PHZ}).

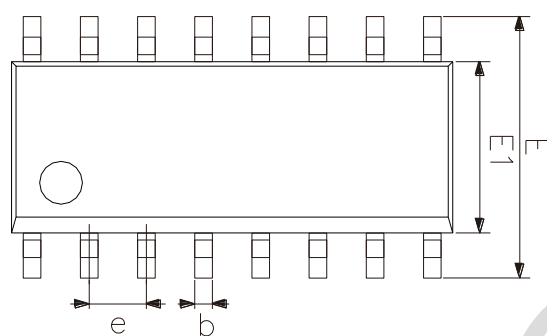
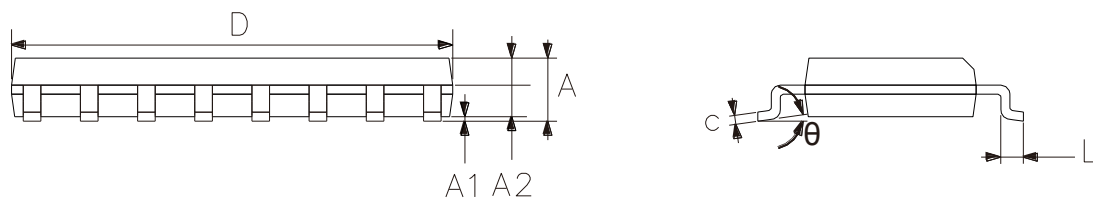
5.4、MeasurementPoints

supply voltage	input		output		
V_{CC}	V_I	V_M	V_M	V_X	V_Y
2.3V to 2.7V	V_{CC}	$0.5 \times V_{CC}$	$0.5 \times V_{CC}$	$V_{OL} + 0.15V$	$V_{OH} - 0.15V$
3.0V to 3.6V	V_{CC}	$0.5 \times V_{CC}$	$0.5 \times V_{CC}$	$V_{OL} + 0.3V$	$V_{OH} - 0.3V$



6、Package Information

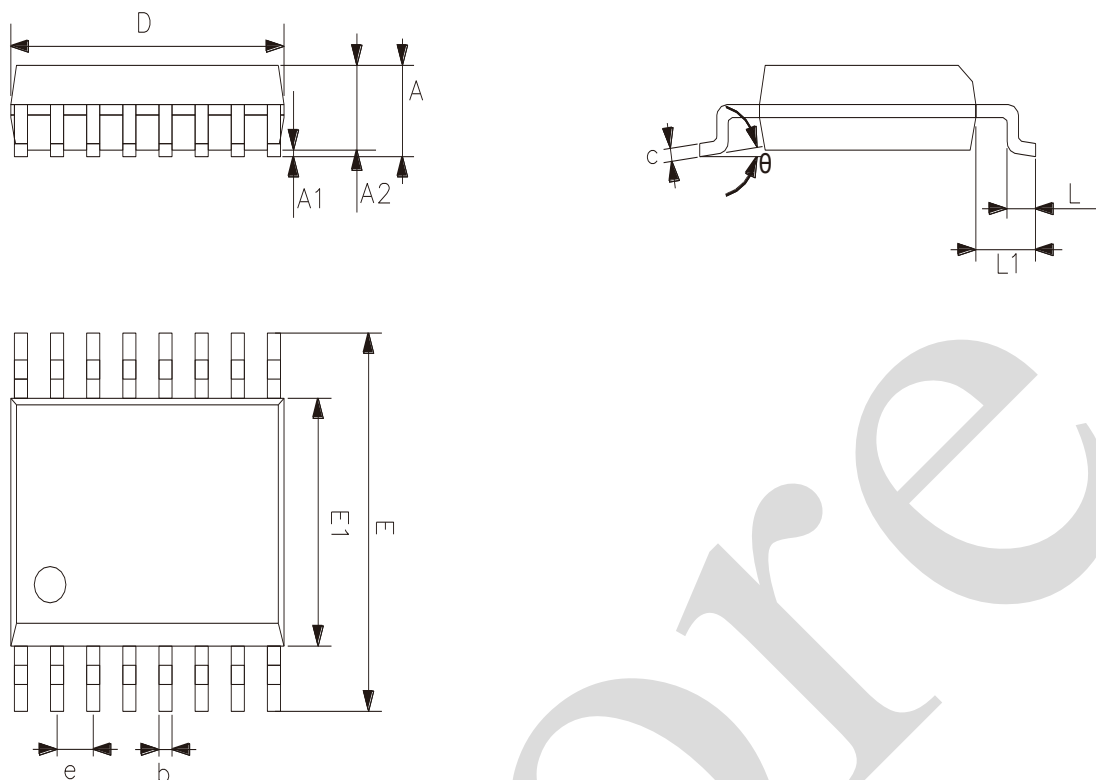
6.1、SOP16



2023/12/A	Dimensions In Millimeters	
Symbol	Min.	Max.
A	1.35	1.80
A1	0.10	0.25
A2	1.25	1.55
b	0.33	0.51
c	0.19	0.25
D	9.50	10.10
E	5.80	6.30
E1	3.70	4.10
e	1.27	
L	0.35	0.89
θ	0°	8°



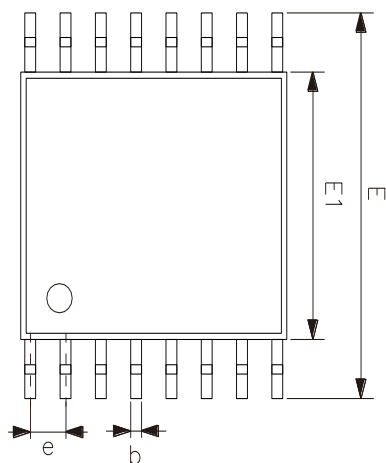
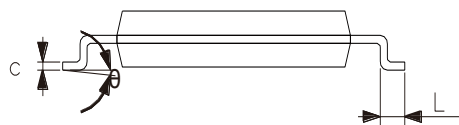
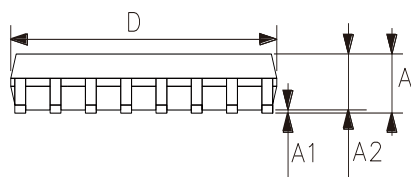
6.2、SSOP16



2023/12/A Symbol	Dimensions In Millimeters	
	Min	Max
A	—	1.75
A1	0.02	0.23
A2	1.30	1.50
b	0.23	0.31
c	0.20	0.24
D	4.70	5.10
E	5.80	6.25
E1	3.80	4.02
e	0.635	
L	0.45	0.80
L1	1.05	
θ	0°	8°



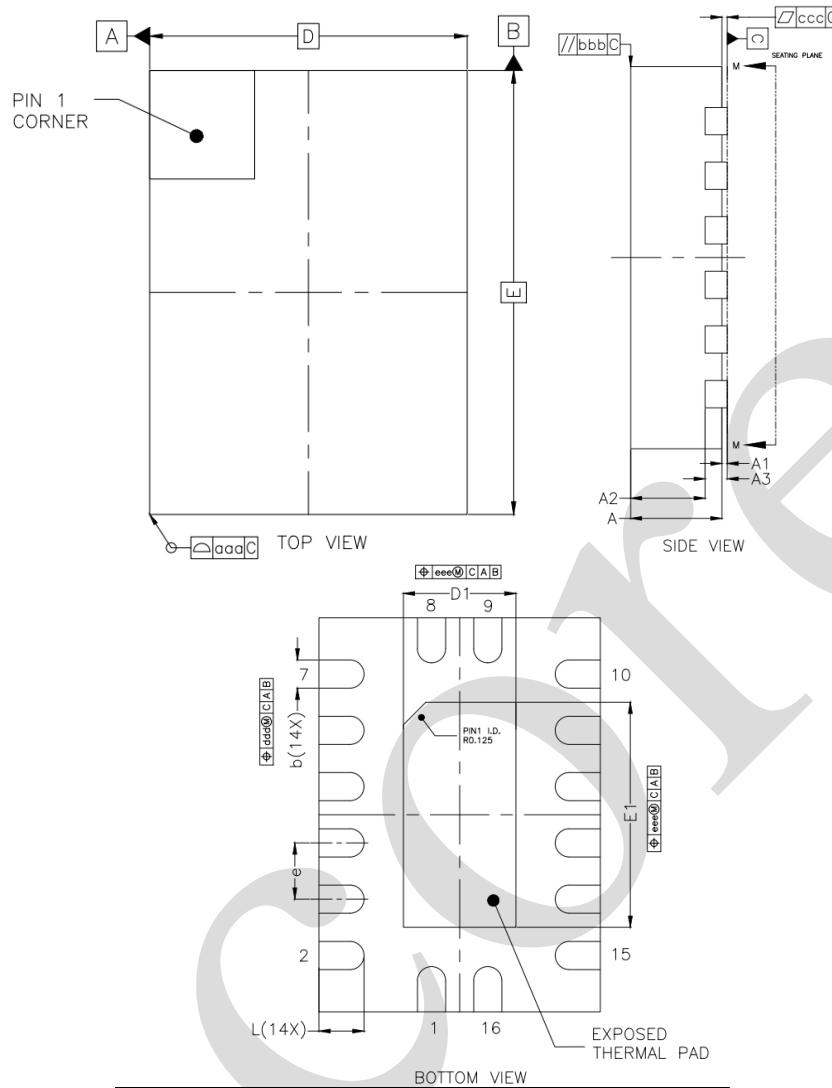
6.3、TSSOP16



2023/12/A	Dimensions In Millimeters	
Symbol	Min	Max
A	—	1.20
A1	0.05	0.15
A2	0.80	1.05
b	0.19	0.30
c	0.09	0.20
D	4.90	5.10
E1	4.30	4.50
E	6.20	6.60
e	0.65	
L	0.45	0.75
θ	0°	8°



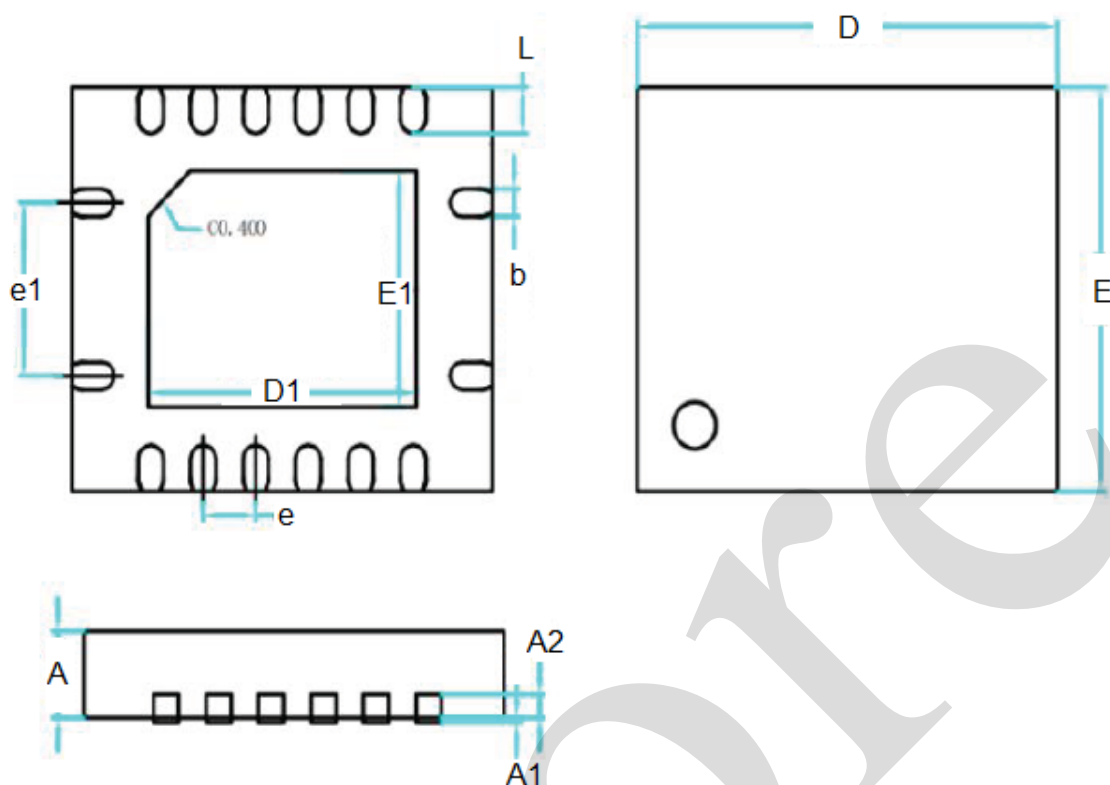
6.4、DHVQFN16



2023/12/A	Dimensions In Millimeters	
Symbol	Min	Max
A	0.80	1.00
A1	0.00	0.05
A2	0.60	0.70
A3	0.20	
D	2.40	2.60
E	3.40	3.60
e	0.50	
b	0.18	0.30
L	0.30	0.50
D1	0.85	1.15
E1	1.85	2.15



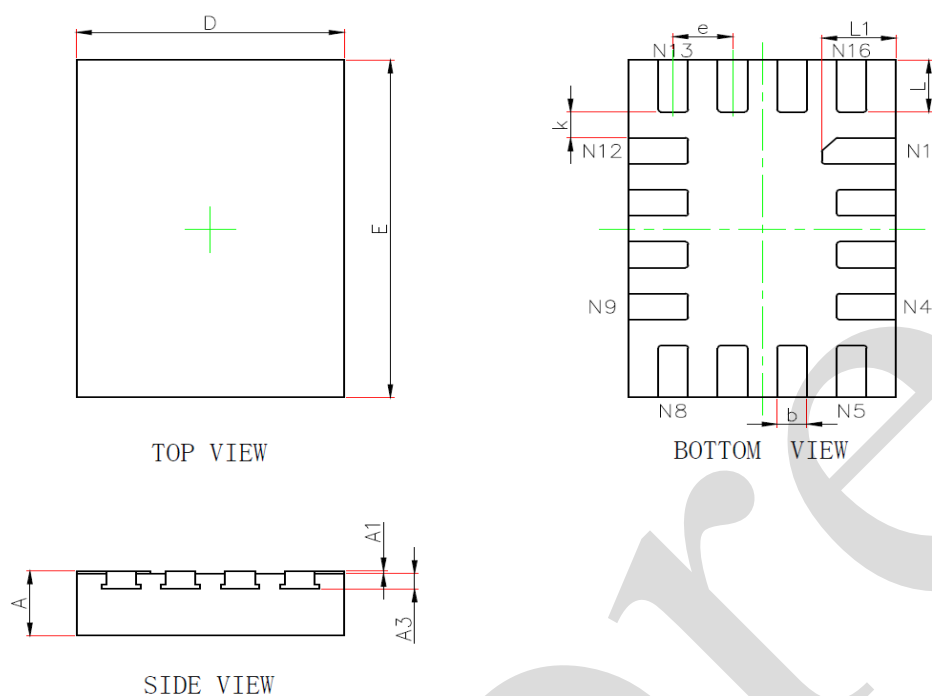
6.5、VQFN16



2023/12/A	Dimensions In Millimeters	
Symbol	Min	Max
A	0.75	
A1	0.05	
A2	0.203	
b	0.215	0.265
D	4.00	
D1	2.525	2.575
E	3.50	
E1	2.025	2.075
e	0.475	0.525
e1	1.475	1.525
L	0.40	



6.6、MIS16



2026/03/B	Dimensions In Millimeters	
Symbol	Min.	Max.
A	0.427	0.550
A1	0	0.05
A3	0.127 REF	
D	1.75	1.85
E	2.55	2.65
k	0.15	
b	0.15	0.25
e	0.40	
L	0.35	0.45
L1	0.45	0.55



7、Statements And Notes

7.1、The name and content of Hazardous substances or Elements in the product

Part name	Hazardous substances or Elements									
	Lead and lead compounds	Mercury and mercury compounds	Cadmium and cadmium compounds	Hexavalent chromium compounds	Polybrominated biphenyls	Polybrominated biphenyl ethers	Dibutyl phthalate	Butylbenzyl phthalate	Di-2-ethylhexyl phthalate	Diisobutyl phthalate
Lead frame	○	○	○	○	○	○	○	○	○	○
Plastic resin	○	○	○	○	○	○	○	○	○	○
Chip	○	○	○	○	○	○	○	○	○	○
The lead	○	○	○	○	○	○	○	○	○	○
Plastic sheet installed	○	○	○	○	○	○	○	○	○	○
explanation	○: Indicates that the content of hazardous substances or elements in the detection limit of the following the SJ/T11363-2006 standard. ×: Indicates that the content of hazardous substances or elements exceeding the SJ/T11363-2006 Standard limit requirements.									

7.2、Notes

We recommend you to read this chapter carefully before using this product.

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